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Z&PINFP10802

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CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper for Serial No. 10/615,567 is being facsimile transmitted to the Patent and Trademark Office on the date shown below.


Signature

ALFRED K. DASSLER

November 4, 2005
DateIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Dietrich Bonart et al.
Applic. No. : 10/615,567 Confirmation No. 9016
Filed : July 8, 2003

Title : Vertical Transistor, and Method for Producing
a Vertical Transistor

Examiner : Mary A. Wilczewski
Group Art Unit : 2822
Docket No. : Z&PINFP10802
Customer No. : 24131

A M E N D M E N T under 37 C.F.R. § 1.116

Mail Stop Box AF
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the final Office action dated August 4, 2005
kindly amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.